

IFW/2154-30154-

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Dated: May 28, 2004

Signature:

(Arnold B. Donfieri)

Docket No.: SCEI 3.0-058
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Suzuoki et al.

Application No.: 09/816,752

Group Art Unit: 2154

Filed: March 22, 2001

Examiner: J. A. Follansbee

For: PROCESSING MODULES FOR COMPUTER
ARCHITECTURE FOR BROADBAND
NETWORKS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is enclosed.

This application is related to U.S. patent application numbers 09/816,004 filed March 22, 2001; 09/815,554 filed March 22, 2001; 09/816,020 (now U.S. patent number 6,526,491); 10/371,322 filed February 21, 2003; and 09/815,558 filed March 22, 2001. The listed references were cited in one or more of these related applications.

Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

Application No.: 09/816,752

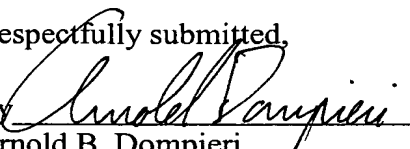
Docket No.: SCEI 3.0-058

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: May 28, 2004

Respectfully submitted,

By



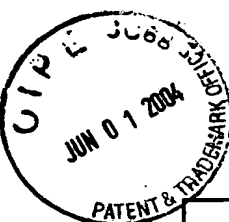
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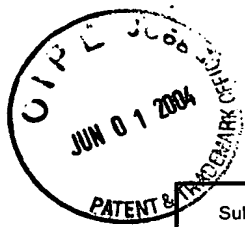


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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	09/816,752
				Filing Date	March 22, 2001
				First Named Inventor	Masakazu Suzuoki
				Art Unit	2154
				Examiner Name	J. A. Follansbee
Sheet	1	of	2	Attorney Docket Number	SCEI 3.0-058

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	AA	US-2002/0078285-A1	06-20-2002	Hofstee, et al.	
	AB	US-2002/0078308-A1	06-20-2002	Altman, et al.	
	AC	US-2002/0087815-A1	07-04-2002	Arimilli et al.	
	AD	US-4,782,443	11-01-1988	Matsumoto	
	AE	US-5,355,481	10-11-1994	Sluijter	
	AF	US-5,619,671	04-08-1997	Bryant et al	
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	AL	US-5,900,019	05-04-1999	Greenstein et al.	
	AM	US-6,212,605-B1	04-03-2001	Arimilli et al.	
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	AO	US-6,289,434-B1	09-11-2001	Rupan Roy	
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	AR	US-6,477,170-B1	11-05-2002	Lu, et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
	BA	EP-0 461 926-A2	12-18-1991	Compaq Computer Corporation		*
	BB	EP-0 730 237-A1	09-04-1996	NEC Corporation		*
	BC	GB-2,326,254	12-16-1998	Mitsubishi Denki Kabushiki Kaisha Incorporated		*
	BD	JP-01-217689	08-31-1989	Toshiba Corp.		*
	BE	JP-02-057237	02-27-1990	Hitachi Medical Corp.		*
	BF	JP-04-288643	10-13-1992	NEC Corp.		*
	BG	JP-05-054009	03-05-1993	NEC Engineering, Ltd.		*
	BH	JP-06-012333	01-21-1994	Hitachi Ltd.; Hitachi Computer Engineering Corp. Ltd.		*
	BI	JP-08-180018	07-12-1996	Toshiba Corp.		*
	BJ	JP-08-212178	08-20-1996	Hitachi, Ltd.		*
	BK	JP-08-249261	09-27-1996	Mitsubishi Electric Corporation		*
	BL	JP-09-198361	07-31-1997	Kofu Nippon Denki KK		*
	BM	JP-09-311839	12-02-1997	Hitachi Ltd.		*C
	BN	JP-10-126771	05-15-1998	Toshiba Corp.		*
	BO	JP-11-003306	01-06-1999	Mitsubishi Electric Corp.		*
	BP	JP-11-039215	02-12-1999	Matsushita Electric Ind. Co. Ltd.		*
	BQ	JP-11-232247	08-27-1999	Hitachi Ltd.		*
	BR	JP-11-338833	12-10-1999	Hitachi Ltd.		*
	BS	JP-2000-057329	02-25-2000	IBM Corp.		*



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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
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				Art Unit	2154
				Examiner Name	J. A. Follansbee
Sheet	2	of	2	Attorney Docket Number	SCEI 3.0-058

	BT	JP-2000-222384	08-11-2000	Yamatake Corp		*
	BU	JP-2002-032218	01-31-2002	Rikogaku Shinkokai		*
	BV	JP-54-012643	01-30-1979	Hitachi Ltd.		*
	BW	JP-56-111962	09-04-1981	Agency of Ind. Science & Technology		*
	BX	JP-57-006952	01-13-1982	Nippon Telegram & Telephone Corp.		*
	BY	JP-64-012364	01-17-1989	Nippon Telegram & Telephone Corp.		*
	BZ	JP-64-023342	01-26-1989	Mitsubishi Electric Corp.		*

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **CITE NO.: Those patent(s) or publication(s) which are marked with an double asterisk (**) next to the Cite No. are not supplied because they were previously cited by or submitted to the Office in a prior application relied upon in this application for an earlier filing date under 35 U.S.C. 120. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				T ²
	CA	MASAKAZU SUZUOKI, Playstation 2 Yur Maikuropurosessesa Emotion Engine (Microprocessor Emotion Engine for PlayStation 2), bit, Kyoritsu Shuppan Co, Ltd., 01-01-2000, pages 11-18, Vol. 32, No. 1.				
	CB	Baer, et al. "On the Inclusion Properties for Multi-Level Cache Hierarchies," Proceedings of the Annual International Symposium on Computer Architecture, Honolulu, Hawaii, May 30 - June 2, 1988, Washington, IEEE Computer Society Press, pp. 73-80.				
	CC	Mamoru Maekawa, et al., "Distributed Operating System -- That Coming Next to UNIX," 1st Ed., Dec. 25, 1991, Kyoritsu Shuppan Co., Ltd.				✓
	CD	"IBM System/370 Extended Architecture Instruction Manual," 1st Ed., IBM Japan, April 1984, pp. 3-8.				✓
	CE	William J. Dally, et al., "The Message-Driven Processor: A Multicomputer Processing Node with Efficient Mechanisms," IEEE Micro, April, 1992, pp. 23-39.				
	CF	Ted Painter, et al., "Perceptual Coding of Digital Audio," Proceedings of the IEEE, Vol. 88, No. 4, Apr. 2000, pp. 451-513.				

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered	
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* Translation of Abstract only.

*C = Includes U.S. patent counterpart.